

EAST - [default.wsp.1]

File View Edit Tools Window Help

☐ Drafts  
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    L1: (39) first same second same external same signals sam  
    L2: (362) external same signals same (power adj up)  
    L3: (343) 2 not 1  
    L4: (267) 3 and voltage  
    L5: (109) 4 and (voltage same enable)  
☐ Failed  
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    (28) (first same second same external same signals same ( )  
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DB: USPAI  
Data operate: OR  
1 and (voltage same enable)

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	3
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6338127 B1	20020108	44	Method and apparatus for resynchronizing a plurality	711/167	713/400		Manning, Troy A.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6282145 B1	20010828	74	Array architecture and operating methods for	365/230.06	365/51 ; 365/63		Tran, Hieu Van ; et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6279090 B1	20010821	62	Method and apparatus for resynchronizing a plurality	711/167	711/105 ; 711/109		Manning, Troy A.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6166775 B1	20010731	23	Dual capacitor oscillator circuit	331/111	102/215 ; 102/218		Patti, Robert S.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US RE37195 E	20010529	41	Programmable switch for FPGA 326/39		326/41		Kean, Thomas A.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6230970 B1	20010515	44	Low-power hand-held transaction device	235/379	235/380 ; 235/472.01		Walsh, Joseph P. ; et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6225789 B1	20010501	15	Battery charger	320/162	320/137		Yuen, Tang Kong	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6208148 B1	20010327	16	Battery charger	324/433	370/455		Yuen, Tang Kong	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6172518 B1	20010109	21	Method of minimizing power use in programmable logic	326/37	326/39		Jenkins, IV., Jesse H. ; et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6134612 A	20001017	182	External modular bay for housing I/O devices	710/62	710/2		Bailey, Larry D. ; et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6128213 A	20001003	55	Nonvolatile ferroelectric memory and a method of	365/145	365/185.23 ; 365/230.03		Kang, Hee Bok	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input type="checkbox"/>	<input type="checkbox"/>	US 6126185 A	20001003	185	Peripheral card locking	361/683	70/58		Fuhrs, Eric D.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

